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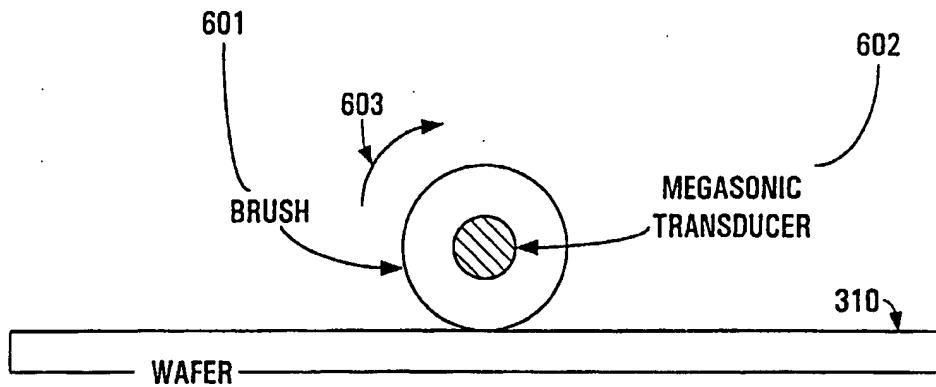


INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 7 : <b>H01L 21/00</b>	A1	(11) International Publication Number: <b>WO 00/59006</b> (43) International Publication Date: 5 October 2000 (05.10.00)
(21) International Application Number: PCT/US00/07394 (22) International Filing Date: 20 March 2000 (20.03.00)  (30) Priority Data: 09/281,619 30 March 1999 (30.03.99) US  (71) Applicant: PHILIPS ELECTRONICS NORTH AMERICA CORPORATION [US/US]; 1251 Avenue of the Americas, New York, NY 10020-1104 (US).  (72) Inventors: ZHANG, Liming; 1069 Greco Avenue #B214, Sunnyvale, CA 94087 (US). WELING, Milind, G.; 1510 Ambergrove Drive, San Jose, CA 95131 (US).  (74) Agents: GALENISON, Mavis, S. et al.; Ladas & Parry, 5670 Wilshire Boulevard, Suite 2100, Los Angeles, CA 90036-5679 (US).	(81) Designated States: AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).  Published <i>With international search report.</i>	

(54) Title: SEMICONDUCTOR WAFER CLEANING APPARATUS AND METHOD

600



(57) Abstract

A cleaning apparatus for removing contaminants from the surface of a semiconductor wafer. A megasonic nozzle and a scrubbing brush are included in the cleaning apparatus. The megasonic nozzle is adapted for outputting megasonically agitated fluid to dislodge contaminant particles from a surface of a semiconductor wafer. The scrubbing brush is adapted to contact the surface of the semiconductor wafer and frictionally remove therefrom the contaminant particles. The megasonic nozzle and the scrubbing brush are both mounted within a cleaning assembly. The cleaning assembly simultaneously employs both the megasonic nozzle and brush to efficiently clean the contaminant particles from the surface of the semiconductor wafer.

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## SEMICONDUCTOR WAFER CLEANING APPARATUS AND METHOD

### FIELD OF THE INVENTION

The field of the present invention pertains to semiconductor fabrication processing. More particularly, the present invention relates to a device for more effective post CMP wafer cleaning. In one embodiment, there is disclosed a post CMP megasonic and brush-based semiconductor wafer cleaning.

### BACKGROUND OF THE INVENTION

Most of the power and usefulness of today's digital IC devices can be attributed to the increasing levels of integration. More and more components (resistors, diodes, transistors, and the like) are continually being integrated into the underlying chip, or IC. The starting material for typical ICs is very high purity silicon. The material is grown as a single crystal. It takes the shape of a solid cylinder. This crystal is then sawed (like a loaf of bread) to produce wafers typically 10 to 30 cm in diameter and 250 microns thick.

The geometry of the features of the IC components are commonly defined photographically through a process known as photolithography. Very fine surface geometries can be reproduced accurately by this technique. The photolithography process is used to define component regions and build up components one layer on top of another. Complex ICs can often have many different built-up layers, each layer having components, each layer having differing interconnections, and each layer stacked on top of the previous layer. The resulting topography of

these complex IC's often resemble familiar terrestrial "mountain ranges," with many "hills" and "valleys" as the IC components are built up on the underlying surface of the silicon wafer.

- 5        In the photolithography process, a mask image, or pattern, defining the various components is focused onto a photosensitive layer using ultraviolet light. The image is focused onto the surface using the optical means of the photolithography tool and is imprinted into the photosensitive layer. To build ever smaller features, increasingly fine
- 10      images must be focused onto the surface of the photosensitive layer, e.g. optical resolution must increase. As optical resolution increases, the depth of focus of the mask image correspondingly narrows. This is due to the narrow range in depth of focus imposed by the high numerical aperture lenses in the photolithography tool. This narrowing depth of
- 15      focus is often the limiting factor in the degree of resolution obtainable and, thus, the smallest components obtainable using the photolithography tool. The extreme topography of complex ICs, the "hills" and "valleys," exaggerate the effects of decreasing depth of focus. Thus, in order properly to focus the mask image defining sub-micron
- 20      geometries onto the photosensitive layer, a precisely flat surface is desired. The precisely flat (e.g. fully planarized) surface will allow for extremely small depths of focus and, in turn, allow the definition and subsequent fabrication of extremely small components.
- 25      Chemical-mechanical polishing (CMP) is the preferred method of obtaining full planarization of a wafer. It involves removing a sacrificial layer of dielectric material or metal using mechanical contact between

the wafer and a moving polishing pad with chemical assistance from a polishing slurry. Polishing flattens out height differences since high areas of topography (hills) are removed faster than areas of low topography (valleys). CMP is the only technique with the capability of

5 smoothing out topography over millimeter scale planarization distances leading to maximum angles of much less than one degree after polishing.

Figure 1 shows a down view of a typical prior art CMP machine

10 100 and Figure 2 shows a side cut away view of the CMP machine 100. The CMP machine 100 is fed wafers to be polished. The CMP machine 100 picks up the wafers with an arm 101 and places them onto a rotating polishing pad 102. The polishing pad 102 is made of a resilient material and is typically textured, often with a plurality of predetermined grooves

15 103, to aid the polishing process. The polishing pad 102 rotates on a platen 104, or turn table, located beneath the polishing pad 102, at a predetermined speed. A wafer 105 is held in place on the polishing pad 102 and the arm 101 by a carrier ring 112 and a carrier 106. The lower surface of the wafer 105 (e.g., the "front" side) rests against the polishing

20 pad 102. The upper surface of the wafer 105 is against the lower surface of the carrier 106 of the arm 101. As the polishing pad 102 rotates, the arm 101 rotates the wafer 105 at a predetermined rate. The arm 101 forces the wafer 105 into the polishing pad 102 with a predetermined amount of down force. The CMP machine 100 also includes a slurry

25 dispense arm 107 extending across the radius of the polishing pad 102. The slurry dispense arm 107 dispenses a flow of slurry onto the polishing pad 102.

The slurry is a mixture of de-ionized water and polishing agents designed to aid chemically the smooth and predictable planarization of the wafer. The rotating action of both the polishing pad 102 and the 5 wafer 105, in conjunction with the polishing action of the slurry, combine to planarize, or polish, the wafer 105 at some nominal rate. This rate is referred to as the removal rate. A constant and predictable removal rate is important to the uniformity and performance of the wafer fabrication process. The removal rate should be expedient, yet 10 yield precisely planarized wafers, free from surface topography. If the removal rate is too slow, the number of planarized wafers produced in a given period of time decreases, degrading wafer through-put of the fabrication process. If the removal rate is too fast, the CMP 15 planarization process will not be uniform across the surface of the wafers, degrading the yield of the fabrication process.

To aid in maintaining a stable removal rate, the CMP machine 100 includes a conditioner assembly 120. The conditioner assembly 120 includes a conditioner arm 108, which extends across the radius of the 20 polishing pad 102. An end effector 109 is connected to the conditioner arm 108. The end effector 109 includes an abrasive conditioning disk 110 which is used to roughen the surface of the polishing pad 102. The conditioning disk 110 is rotated by the conditioner arm 108 and is translationally moved towards the center of the polishing pad 102 and 25 away from the center of the polishing pad 102, such that the conditioning disk 110 covers the radius of the polishing pad 102, thereby covering nearly the entire surface area of the polishing pad 102 as the polishing

pad 102 rotates. A polishing pad having a roughened surface has an increased number of very small pits and gouges in its surface from the conditioner assembly 120 and, therefore, produces a faster removal rate via increased slurry transfer to the surface of the wafer and from more 5 effective application of polishing down force. Without conditioning, the surface of polishing pad 102 is smoothed during the polishing process and removal rate decreases dramatically. The conditioner assembly 120 re-roughens the surface of the polishing pad 102, thereby improving the transport of slurry and improving the removal rate.

10

Thus, the action of the rough surface of the polishing pad 102, the chemical softening action of the slurry, and the abrasive action of the slurry combine to polish the wafer 105 such that topography over millimeter scale planarization distances is nearly completely smoothed 15 away. Once CMP is complete, wafer 105 is removed from polishing pad 102 by arm 101 and is prepared for the next phase in the device fabrication process. However, prior to subsequent fabrication processing, wafer 105 must be cleansed of contaminants left over from the CMP process (e.g., particles of polishing pad 102, trace amounts of 20 slurry/abrasives, metal ions, and the like).

After the CMP process is complete, the surface of wafer 105 is cleaned in order to remove particles, metal ions, and other such contaminants. As is well known by those skilled in the art, it is very 25 important that the contaminants left over from the CMP process be removed before wafer 105 proceeds through further fabrication processing. For example, the presence of contaminant particles can

disrupt subsequent lithography, which can lead to, for example, broken lines, shorts, and the like. Currently, the most widely used post-CMP cleaning processes involves the use of brushes, wherein the brushes are frictionally used to scrub the surface of wafer 105 until all contaminants  
5 are removed.

The use of brushes has a distinct disadvantage, however, in that the brush needs to be in direct contact with the surface of wafer 105 in order to wipe off the contaminants effectively. Cleaning effectiveness is  
10 increased with increasing brush pressure against the surface. This is offset by the fact that increased pressure leads to an increased risk of damage to the wafer surface from the scrubbing action of the brush. Additionally, in most cases, the brush itself can be loaded with the contaminants and thereby reduce the cleaning efficiency. Furthermore,  
15 brushes tend to be less effective in the presence of topography within recessed areas (e.g., trenches, holes, etc.).

Because of the above disadvantages associated with brushing for post-CMP cleaning, "megasonic" wafer cleaning techniques have been  
20 developed. As is well known, megasonic cleaning involves the use of a high frequency, extremely agitated stream of fluid (e.g., de-ionized water) which is directed towards the surface of wafer 105. The extreme agitating action of the stream of fluid forcibly dislodges contaminants and polishing byproducts from the wafer surface. The megasonic  
25 cleaning provides the advantage of dislodging contaminants and particles, even in recessed areas, without making direct physical contact with the wafer, thereby greatly reducing the risk of damage to the wafer

surface. A primary disadvantage of megasonic cleaning, however, is the fact that the cleaning action is not as effective as the cleaning action of brushing.

- 5        Thus, what is required is a method and system for efficiently removing CMP contaminants and byproducts from the surface of a wafer after the completion of CMP processing. What is required is a solution for efficient post-CMP cleaning which does not risk damaging the surface of the wafer. What is further required is a solution which
- 10      effectively cleans a post CMP wafer surface of contaminants/byproducts without causing damage. The present invention provides a novel solution to the above requirements.

DISCLOSURE OF THE INVENTION

The present invention provides a method and system for efficiently removing CMP contaminants and byproducts from the surface of a wafer after the completion of CMP processing. The present invention

5     provides for efficient post CMP cleaning that does not risk damaging the surface of the wafer. The present invention effectively cleans a post-CMP wafer surface of contaminants/byproducts without causing damage.

10    In one embodiment, the present invention is implemented as a cleaning apparatus for removing contaminants from the surface of a semiconductor wafer. A megasonic nozzle and a scrubbing brush are included in the cleaning apparatus. The megasonic nozzle is adapted for outputting megasonically agitated fluid to dislodge contaminant

15    particles from a surface of a semiconductor wafer. The scrubbing brush is adapted to contact the surface of the semiconductor wafer and frictionally remove therefrom the contaminant particles. The megasonic nozzle and the scrubbing brush are both mounted within a cleaning assembly. The cleaning assembly simultaneously employs

20    both the megasonic nozzle and brush to clean efficiently the contaminant particles from the surface of the semiconductor wafer. The cleaning assembly's simultaneous use of both megasonically agitated fluid and the scrubbing brush provides for more effective cleaning than using one or the other alone. In this manner, the cleaning assembly effectively

25    cleans the post-CMP wafer surface of contaminants/byproducts without damaging the surface.

BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are incorporated in and form a part of this specification, illustrate embodiments of the invention and, together with the description, serve to explain the principles of the  
5 invention:

Prior art Figure 1 shows a down view of a typical prior art CMP machine.

10 Prior art Figure 2 shows a side view of the prior art CMP machine from Figure 1.

15 Figure 3 shows a diagram of the scrubbing brush being used on a semiconductor wafer, illustrating the brush-based component of the present invention.

Figure 4 shows a diagram of a megasonic transducer being used on a semiconductor wafer, illustrating the megasonic-based component of the present invention.

20

Figure 5 shows a diagram depicting a megasonic-based component to the present invention wherein the megasonic nozzle and megasonic transducer are integrated into a single unit and a spinning motion is imparted to the semiconductor wafer.

25

Figure 6A shows a side view of a combined megasonic/brush cleaning assembly in accordance with one embodiment of present invention.

5       Figure 6B shows a down view of the combined megasonic/brush cleaning assembly from figure 6A.

10      Figure 7A shows a side view of a combined megasonic/brush cleaning assembly in accordance with an alternative embodiment of the present invention.

15      Figure 7B shows a down view of the combined megasonic/brush cleaning assembly from Figure 7A.

15      Figure 8 shows a flowchart of steps of a process in accordance with one embodiment of the present invention.

BEST MODE FOR CARRYING OUT THE INVENTION

Reference will now be made in detail to the preferred embodiments of the invention, a method and system for post-CMP megasonic- and brush-based semiconductor wafer cleaning, examples of which are illustrated in the accompanying drawings. While the invention will be described in conjunction with the preferred embodiments, it will be understood that they are not intended to limit the invention to these embodiments. On the contrary, the invention is intended to cover alternatives, modifications and equivalents, which may be included within the spirit and scope of the invention as defined by the appended claims. Furthermore, in the following detailed description of the present invention, numerous specific details are set forth in order to provide a thorough understanding of the present invention. However, it will be obvious to one of ordinary skill in the art that the present invention may be practiced without these specific details. In other instances, well known methods, procedures, components, and circuits have not been described in detail as not unnecessarily to obscure aspects of the present invention.

The present invention provides a combined megasonic and brush based semiconductor wafer cleaning method and system for efficiently removing CMP contaminants and byproducts from the surface of a wafer after the completion of CMP processing. The present invention provides for efficient post-CMP cleaning that does not risk damaging the surface of the wafer. The present invention effectively cleans a post-CMP wafer surface of contaminants/byproducts without causing damage.

Chemical-mechanical polishing (CMP) is the preferred method of obtaining full planarization of a semiconductor wafer containing devices for fabrication processing. The CMP process involves removing one or 5 more layers of material (e.g., dielectric material, aluminum, tungsten, or copper layers, or the like) using both the frictional contact between the wafer and a moving polishing pad saturated with a polishing slurry and the chemical action of the slurry itself. Polishing through the CMP process flattens out height differences, since high areas of topography 10 (hills) are removed faster than areas of low topography (valleys). The CMP process is the preferred technique with the capability of smoothing out topography over millimeter scale planarization distances leading to maximum angles of much less than one degree after polishing.

15 The frictional contact with the surface of the polishing pad of the CMP machine, the chemical softening action of the slurry, and the abrasive action of the slurry that combine to polish a semiconductor wafer also combine to create large amounts of contaminants and polishing byproducts. These contaminants/byproducts (e.g., particles of 20 polishing pad 102, trace amounts of slurry/abrasives, metal ions, and the like) are spread by the CMP process across the entire surface of the wafer. Once CMP is complete, the wafer is removed from the CMP machine and is prepared for the next phase in the device fabrication process. However, prior to subsequent fabrication processing, the wafer 25 must be cleansed of contaminants/byproducts left over from the CMP process. It is very important that the contaminants left over from the CMP process be removed before the wafer proceeds through further

fabrication processing. For example, the presence of contaminants particles can disrupt subsequent lithography processes, which can lead to, for example, broken lines, shorts, and the like.

5 Referring now to Figure 3, a diagram of a scrubbing brush 300 being used on a semiconductor wafer 310 is shown. Figure 3 shows the scrubbing brush component of a cleaning assembly in accordance with the present invention. As depicted in Figure 3, scrubbing brush 300 rotates in the direction shown by arrow 301. As scrubbing brush 300  
10 rotates, wafer 310 frictionally rotates (e.g., spins) beneath scrubbing brush 300 such that scrubbing brush 300 frictionally contacts the entire surface a wafer 310.

The scrubbing brush 300 is advantageous in that it efficiently  
15 removes those contaminants which come within direct contact with scrubbing brush 300 as it frictionally moves across the surface a wafer 310. Scrubbing brush 300, in this embodiment, is a porous brush saturated with specifically tailored cleaning fluids. The cleaning fluids are tailored in accordance with the materials comprising the surface of  
20 wafer 310 (e.g., metal lines covered with oxide, tungsten in oxide via plugs, copper, etc.). The chemicals contained within the cleaning fluid chemically interact with contaminants on the surface of wafer 310. The cleaning fluids react with the contaminants, yielding a reaction product. The reaction product is removed from the wafer surface by the wiping  
25 force of scrubbing brush 300 as well as the flow of the cleaning fluid.

Referring now to Figure 4, a diagram of a megasonic transducer 400 being used on semiconductor wafer 310 is shown. Figure 4 shows the megasonic nozzle component of a cleaning assembly in accordance with the present invention. As depicted in Figure 4, a nozzle 401

5 dispenses cleaning fluid, shown by arrows 402, onto the surface of wafer 310. As the cleaning fluid leaves nozzle 401, the cleaning fluid is megasonically agitated by megasonic transducer 400. Megasonic transducer 400 functions by imparting high frequency (e.g., 2 MHz or above), high energy vibrations to cleaning fluid 402. These high energy

10 vibrations are imparted to the surface of wafer 310 as cleaning fluid 402 contacts the surface. The force of the high energy vibrations within the cleaning fluid 402 dislodges contaminant particles from the surface of wafer 310.

15 A primary distinction between the megasonic component of the present invention and the brush component is the fact that the surface of wafer 310 is cleaned without any direct physical contact with megasonic nozzle 401 or megasonic transducer 400. With the megasonic component, the force which removes the contaminants is the force of the

20 megasonic agitated cleaning fluid 402 (e.g., cavitation, pressure gradients, streaming effects, and the like). The pressure gradient developed by the megasonic waves dislodges and lifts the contaminants from the surface of wafer 310. The streams and the bulk cleaning fluid flow then carry the contaminants away.

25

Figure 5 shows a version of the megasonic component of the present invention wherein the megasonic transducer and the nozzle are

integrated into a single megasonic nozzle 500. As depicted in Figure 5, the megasonic nozzle 500 traverses to the left and to the right of wafer 310, as shown by line 501. Additionally, wafer 310 spins beneath megasonic nozzle 500 as it moves back and forth across its surface. Megasonic

5 nozzle 500 functions in a substantially similar manner as megasonic nozzle 401 and megasonic transducer 400 of Figure 4. However, megasonic nozzle 500 uses both its own movement and the spinning movement of wafer 310 to cover more readily the surface of wafer 310.

10 Referring now to Figures 6A and 6B, a side view and a down view of a combined megasonic/brush cleaning assembly 600 in accordance with the present invention is shown. As depicted in Figures 6A and 6B, the cleaning assembly 600 depicts both the megasonic component and the brush component (e.g., megasonic transducer 602 and brush 601) as

15 combined within a cleaning assembly 600. The megasonic transducer 602 is located coaxially within brush 601. Brush 601 rotates in the direction shown by arrow 603. Wafer 310 rotates frictionally beneath brush 601.

20 In accordance with the present invention, megasonic transducer 602 is located coaxially, inside the brush 601. Megasonic energy is transmitted through the brush 601 and through the cleaning fluid to the surface of wafer 310. The simultaneous cleaning effect of the scrubbing action of brush 601 and the megasonic energy of megasonic transducer

25 602 provides numerous advantages. One such advantage is that the aggregate force of both the brushing component and the megasonic component is significantly stronger than mere brushing or megasonic

fluid alone and, thus, is more effective in removing contaminants.

Another advantage is that there is less chance of "brush loading" with the megasonic energy penetrating into the brush 601 while the wiping action of the brush 601 is being used. Brush loading refers to the fact

5 that, without the application of megasonic energy from megasonic transducer 602, brush 601 could pick up significant amounts of contaminants from wafer 310 such that brush 601 could no longer effectively remove all of the contaminants from the wafer surface (e.g., trying to clean the wafer with a "dirty" brush).

10

Yet another advantage of the cleaning assembly 600 of the present invention is that by combining the action of brush 601 with megasonic transducer 602 to yield a high cleaning efficiency, it is possible to optimize the pressure of the brush 601 and the energy of the megasonic transducer 602 so that the possibility of damage to the surface of wafer 310 during cleaning will be minimized. The high cleaning efficiency allows less pressure to be applied by brush 601 to the surface of wafer 310 in comparison to brushing alone. Lower amounts of brush pressure reduce the risks of frictional damage to the surface of wafer 310.

20

Another advantage of the cleaning assembly 600 the present invention is that the combining of a brush cleaning process with a megasonic cleaning process within a single cleaning assembly 600 reduces both the number of cleaning steps and the amount of factory floor space required, as opposed to a case where a brush cleaning tool and a megasonic cleaning tool are used separately, one after the other. The cleaning steps and time are reduced by operating both scrubbing

and megasonic cleaning simultaneously, thereby improving wafer throughput. Additionally, a cleaning assembly 600 tool in accordance with the present invention will have a smaller footprint.

5        In typical use, megasonic frequencies beyond 500KHz are most effective for post-CMP cleaning. Cleaning efficiency could be optimized by choice of solution chemistry and flow, megasonic process conditions of frequency and power, and scrubbing process conditions of brush pressure, rotational speed and time.

10

It should be noted that various cleaning solutions can be used by the cleaning assembly 600 depicted in Figures 6A and 6B. Cleaning fluids can be tailored to the chemical makeup of the surface of wafer 310, wherein specific chemicals are included within a cleaning solution in 15 order to induce specific cleaning effects upon materials of the surface of wafer 310 (e.g., dielectric, various types of metal, etc.). Alternatively, ordinary de-ionized water can be used as the cleaning fluid.

20      The disadvantage of using scrubbing alone is that the brush needs to be in direct contact with the wafer surface in order effectively to wipe off the contaminants. The brush may damage the wafer surface if a high pressure is exerted. In most cases, the brush itself can be loaded with the contaminants and thereby reduce the cleaning efficiency.

.25      The disadvantage in using megasonic cleaning alone is that the force in a megasonic cleaner is not as effective as the direct wiping in a scrubber; therefore, megasonic cleaning is not as popular as scrubbing

in post-CMP applications. High energy megasonic cleaning, which employs very high energy levels to compensate for the less effective cleaning action, can also damage the wafer surface, for example, when the cavitation occurs at or very close to the wafer surface.

5

Referring now to Figures 7A and 7B, a side view and a down view of a combined megasonic/brush cleaning assembly 700 in accordance with an alternate embodiment of the present invention are shown. As depicted in Figures 7A and 7B, the cleaning assembly 700 depicts two

- 10 megasonic components 702 and 703 and a brush component 701 combined within a cleaning assembly 700. The megasonic transducers 702 and 703 are located alongside the brush 701 as opposed to being coaxially within brush 701. Brush 701 rotates in the direction shown by arrow 704. Wafer 310 spins frictionally beneath brush 701 as shown by
- 15 the direction of spin 705.

Cleaning assembly 700 shows one variation in accordance with the present invention. Cleaning assembly 700 functions in substantially the same manner as cleaning assembly 600 of Figures 6A and 6B.

- 20 However, cleaning assembly 700 locates two transducers alongside the scrubbing brush (e.g., brush 701) as opposed to within the scrubbing brush. In so doing, megasonic transducers 702 and 703 impart megasonic energy to the surface of wafer 310 in a more direct manner. To ensure even coverage, wafer 310 spins beneath brush 701 and
- 25 transducers 702 and 703. In the same manner as cleaning assembly 600, cleaning assembly 700 uses the wiping action of brush 701 and the

megasonic energy of transducers 702 and 703 to provide for safe, high efficiency, low footprint post-CMP cleaning.

Referring now to Figure 8, a flowchart of the steps of a process 800

5 in accordance with one embodiment of present invention are shown.

Process 800 shows the steps involved in the operating process of a post CMP cleaning tool employing a cleaning assembly (e.g., cleaning assembly 600 of Figures 6A and 6B) of the present invention. Process 800 shows steps from receiving wafer for post-CMP cleaning to sending the  
10 wafer to the next step in fabrication process.

Process 800 begins in step 801, where a wafer is received for cleaning after the wafer has been processed in a CMP machine. As described above, chemical mechanical polishing involves the use of  
15 slurries and frictional contact with a polishing pad. The CMP process leads to large amounts of contaminants which must be removed from the surface of the wafer.

In step 802, the wafer is placed within the cleaning assembly of a  
20 post-CMP cleaning tool in accordance with the present invention. The cleaning assembly (e.g., cleaning assembly 600) includes both a scrubbing brush (e.g., brush 601) and a megasonic transducer (e.g., megasonic transducer 602).

25 In step 803, cleaning fluid is dispensed onto the wafer. As described above, the cleaning fluid can be a specialized cleaning solution containing various chemicals specifically tailored for the chemical

makeup of the wafer surface, or alternatively, the cleaning fluid can be ordinary de-ionized water.

In step 804, the surface of the wafer is brushed using the

5 scrubbing brush mounted within the cleaning assembly. As described above, the scrubbing brush uses a wiping action to remove the contaminants on the surface of the wafer.

In step 805, megasonic cleaning fluid is dispensed onto the wafer

10 using a megasonic nozzle mounted within the cleaning assembly. As described above, the cleaning fluid is megasonically agitated by the megasonic nozzle at high frequencies (e.g., 500 kHz or above). The megasonic energy functions by dislodging contaminant particles from the surface of the wafer.

15

Referring still to Figure 8, in step 806, contaminants are removed from the surface the wafer by the combined action of the brush and the megasonic nozzle of the cleaning assembly. As described above, the combination of the megasonic energy and the wiping action of the brush

20 provides for more efficient cleaning than either of the two alone. The high efficiency cleaning action of the cleaning assembly allows less energy to be used with the megasonic nozzle and allows less pressure to be applied with the scrubbing brush in comparison to prior art cleaning methods.

25

In step 807, the surface of a wafer is rinsed using de-ionized water. The rinse is to remove any remaining cleaning fluid leftover after the cleaning process.

5        In step 808, the wafer is spun dry. Once the cleaning fluid has been rinsed away in step 807, the spin dry yields a completely clean wafer, free of all contaminants.

10      In step 809, the completely clean wafer is removed from the cleaning assembly and sent from the post-CMP cleaning tool to the next step in the device fabrication process.

Thus, the present invention provides a method and system for efficiently removing CMP contaminants and byproducts from the 15 surface of a wafer after the completion of CMP processing. The present invention provides for efficient post-CMP cleaning that does not risk damaging the surface of the wafer. The present invention effectively cleans a post-CMP wafer surface of contaminants/byproducts without causing damage.

20

The foregoing descriptions of specific embodiments of the present invention have been presented for purposes of illustration and description. They are not intended to be exhaustive or to limit the invention to the precise forms disclosed, and obviously many 25 modifications and variations are possible in light of the above teaching. The embodiments were chosen and described in order best to explain the principles of the invention and its practical application, thereby to enable

others skilled in the art best to utilize the invention and various embodiments with various modifications as are suited to the particular use contemplated. It is intended that the scope of the invention be defined by the Claims appended hereto and their equivalents.

CLAIMS

What is claimed is:

1. A cleaning apparatus for removing contaminants from the surface of a semiconductor wafer, comprising:
  - a megasonic transducer adapted for outputting megasonically agitated fluid to dislodge contaminant particles from a surface of a semiconductor wafer;
  - a brush adapted to contact the surface of the semiconductor wafer and remove therefrom the contaminant particles; and
  - a cleaning assembly mounting the megasonic transducer and the brush, the cleaning assembly operable for simultaneously employing the megasonic transducer and the brush to clean the contaminant particles from the surface of the semiconductor wafer.
2. The apparatus of Claim 1, wherein the megasonic transducer is adapted to output a megasonically agitated stream of fluid into contact with the surface of the semiconductor wafer to dislodge the contaminant particles.
3. The apparatus of Claim 1, wherein the brush is adapted to contact the surface of the wafer and frictionally remove the contaminant particles by using a wiping action.
4. The apparatus of Claim 1, wherein the cleaning assembly is adapted to laterally move the semiconductor wafer with respect to the brush as the brush cleans the surface of the semiconductor wafer.
5. The apparatus of Claim 1 wherein the apparatus is a post CMP wafer cleaning machine for removing post CMP contaminant particles from the surface of the semiconductor wafer, the brush removing the contaminant particles from the surface of the wafer by using a wiping action, the cleaning assembly being adapted to laterally move the semiconductor wafer with respect to the brush as the brush cleans the surface of the semiconductor wafer.
6. The apparatus of Claim 1 or 5 wherein the megasonic transducer is disposed coaxially within the brush such that the megasonically agitated fluid flows into contact with the surface of the wafer via the brush.
7. The apparatus of Claim 1 or 5, wherein the megasonic transducer is

disposed laterally alongside the brush to flow the megasonically agitated fluid into contact with the surface of the semiconductor wafer.

8. The apparatus of Claim 1 or 5, wherein the cleaning assembly is adapted to spin dry the semiconductor wafer after cleaning.

9. The apparatus of Claim 1 or 5, wherein the fluid is de-ionized water.

10. The apparatus of Claim 1 or 5, wherein the fluid is a cleaning solution tailored chemically interact with the surface of the semiconductor wafer.

11. A method for post CMP cleaning of a semiconductor wafer, the method comprising the steps of:

dislodging contaminant particles from a surface of a semiconductor wafer by using a megasonic transducer adapted to output a stream of megasonically agitated fluid;

remove contaminant particles from the surface of the semiconductor wafer by using a brush adapted to contact the surface of the semiconductor wafer; and

simultaneously employing the megasonic transducer and the brush to clean the contaminant particles from the surface of the semiconductor wafer, the cleaning performed by a cleaning assembly mounting the megasonic transducer and the brush.

12. The method of Claim 11, wherein the megasonic transducer is disposed coaxially within the brush such that the megasonically agitated fluid flows into contact with the surface of the wafer via the brush.

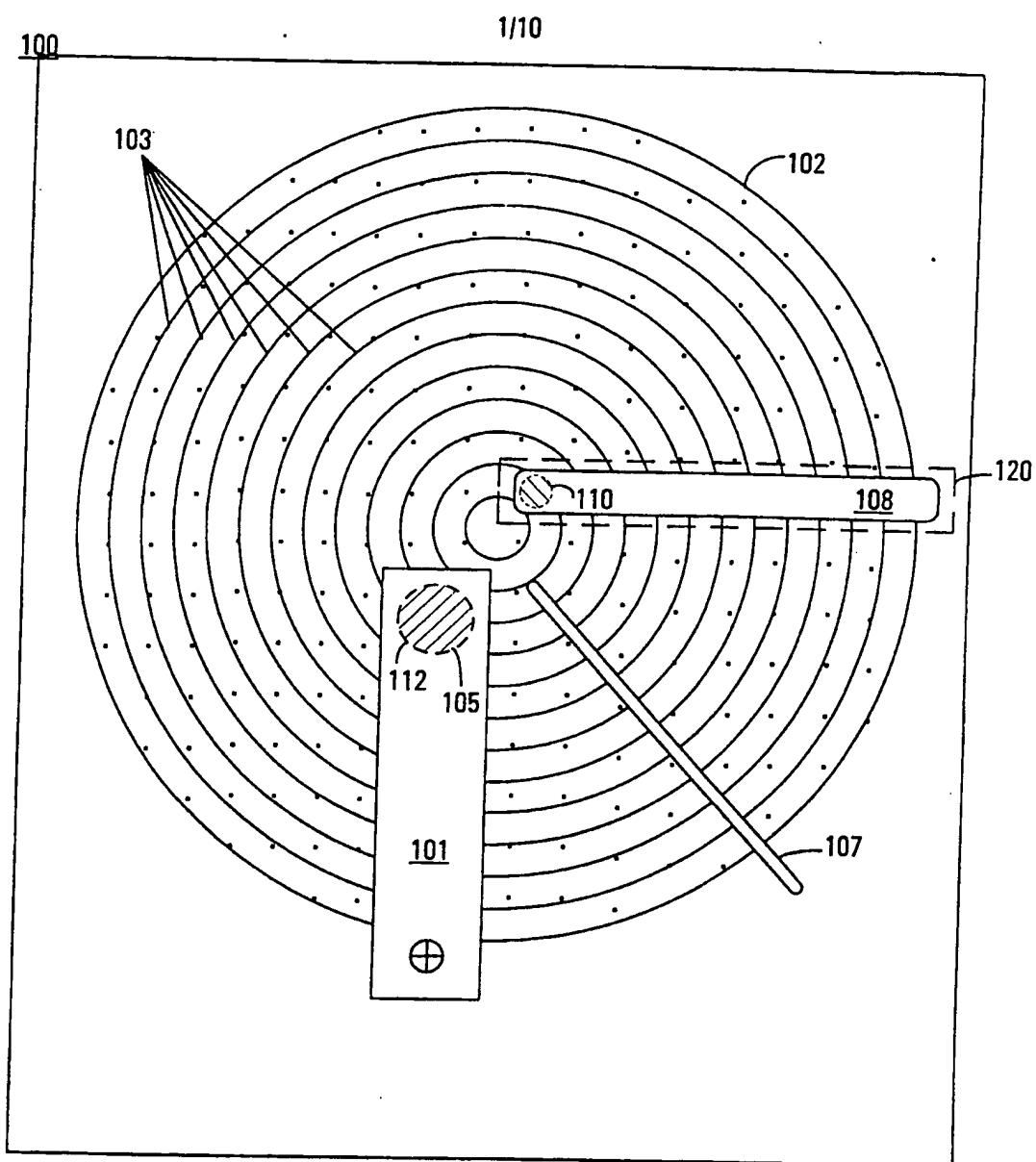
13. The method of Claim 11, further including the step of:

laterally moving the semiconductor wafer with respect to the brush as the brush cleans the surface of the semiconductor wafer, the lateral moving performed by the cleaning assembly.

14. The method of Claim 11, wherein the megasonic transducer is disposed laterally alongside the brush to flow the megasonically agitated fluid into contact with the surface of the semiconductor wafer.

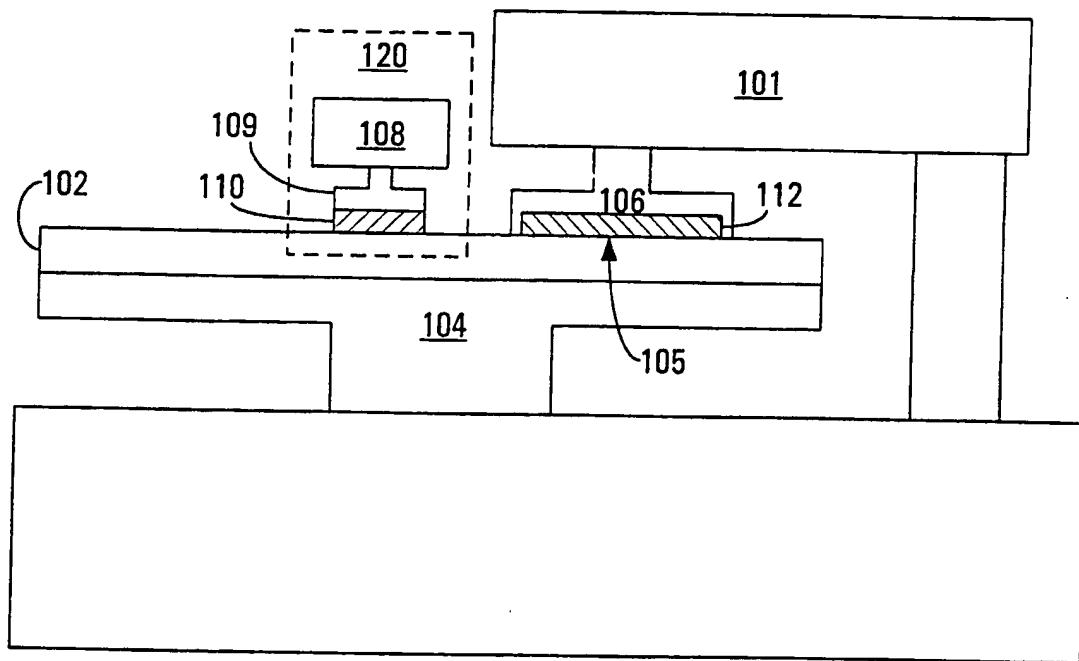
15. The method of Claim 11, further including the step of:

spin drying the semiconductor wafer after cleaning by using the cleaning assembly.



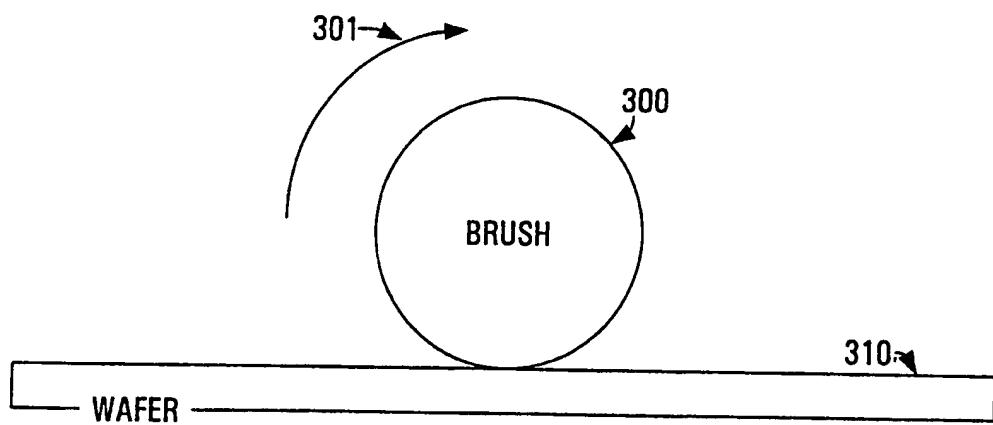
**FIGURE 1**  
(Prior Art )

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**FIGURE 2**  
( Prior Art )

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# FIGURE 3

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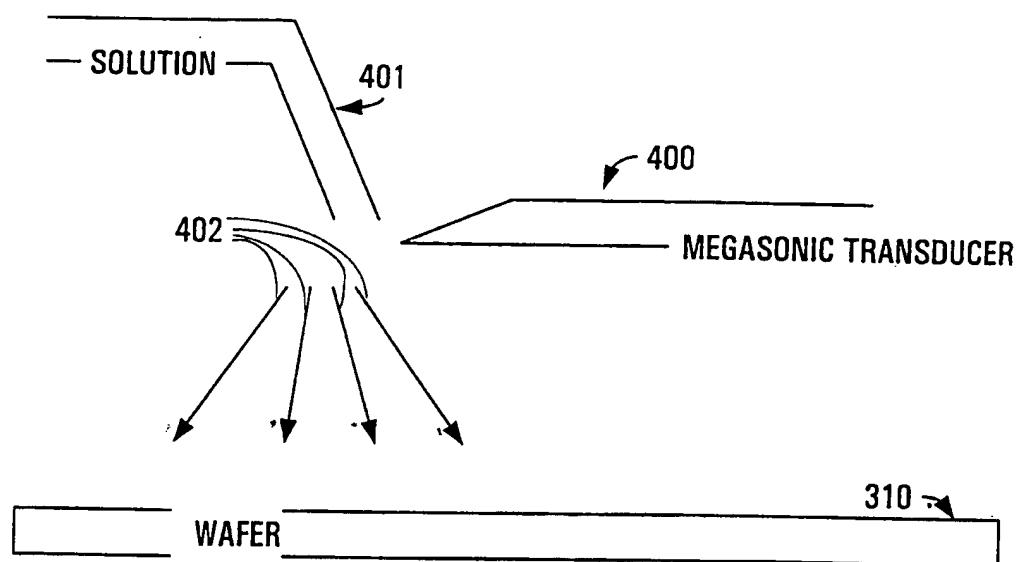
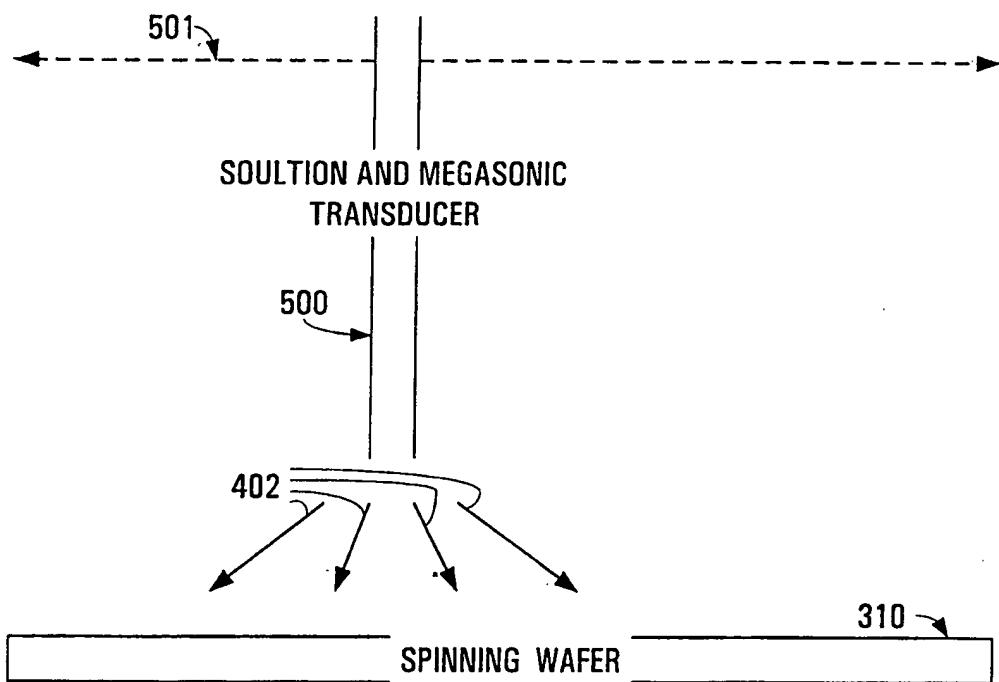


FIGURE 4

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# FIGURE 5

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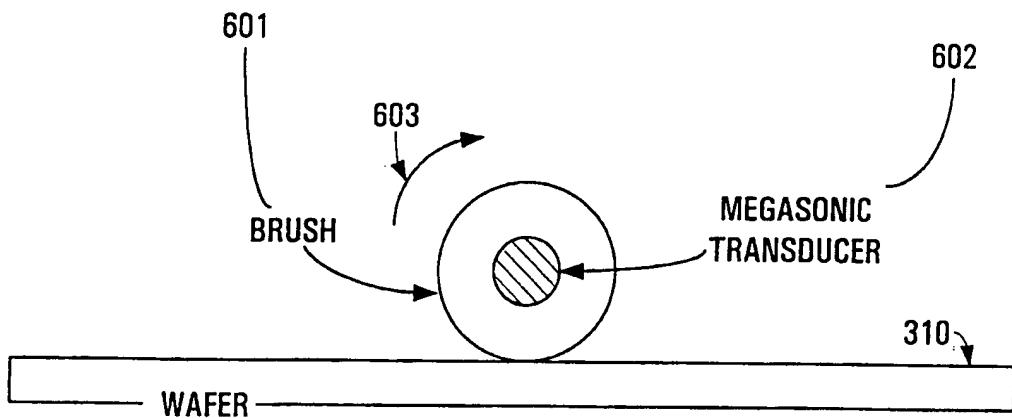
600

FIGURE 6A

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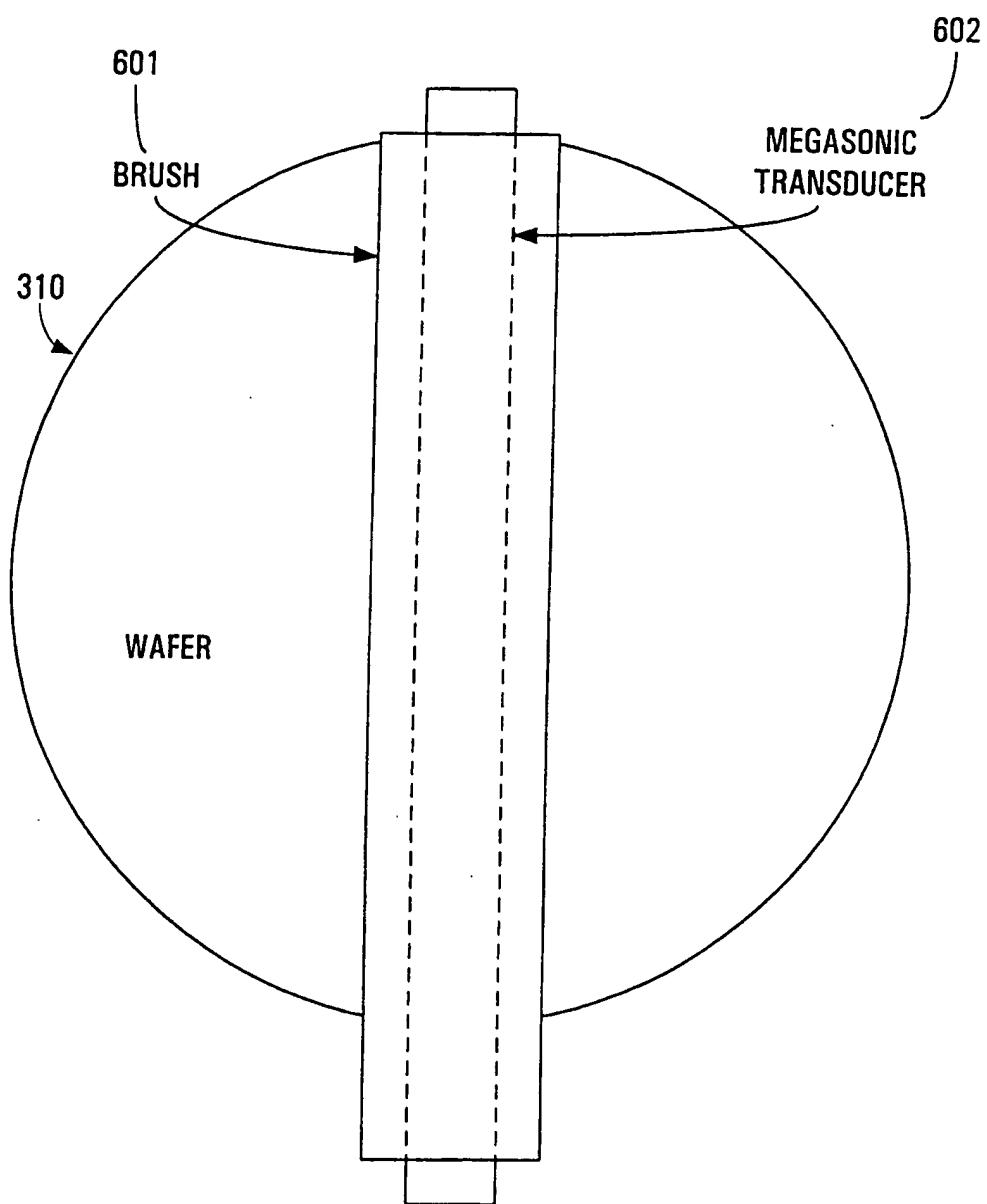
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FIGURE 6B

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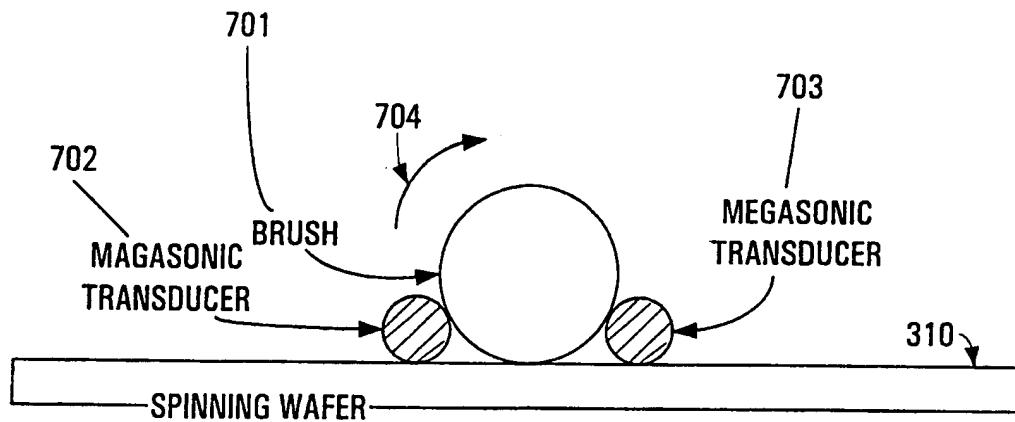
700

FIGURE 7A

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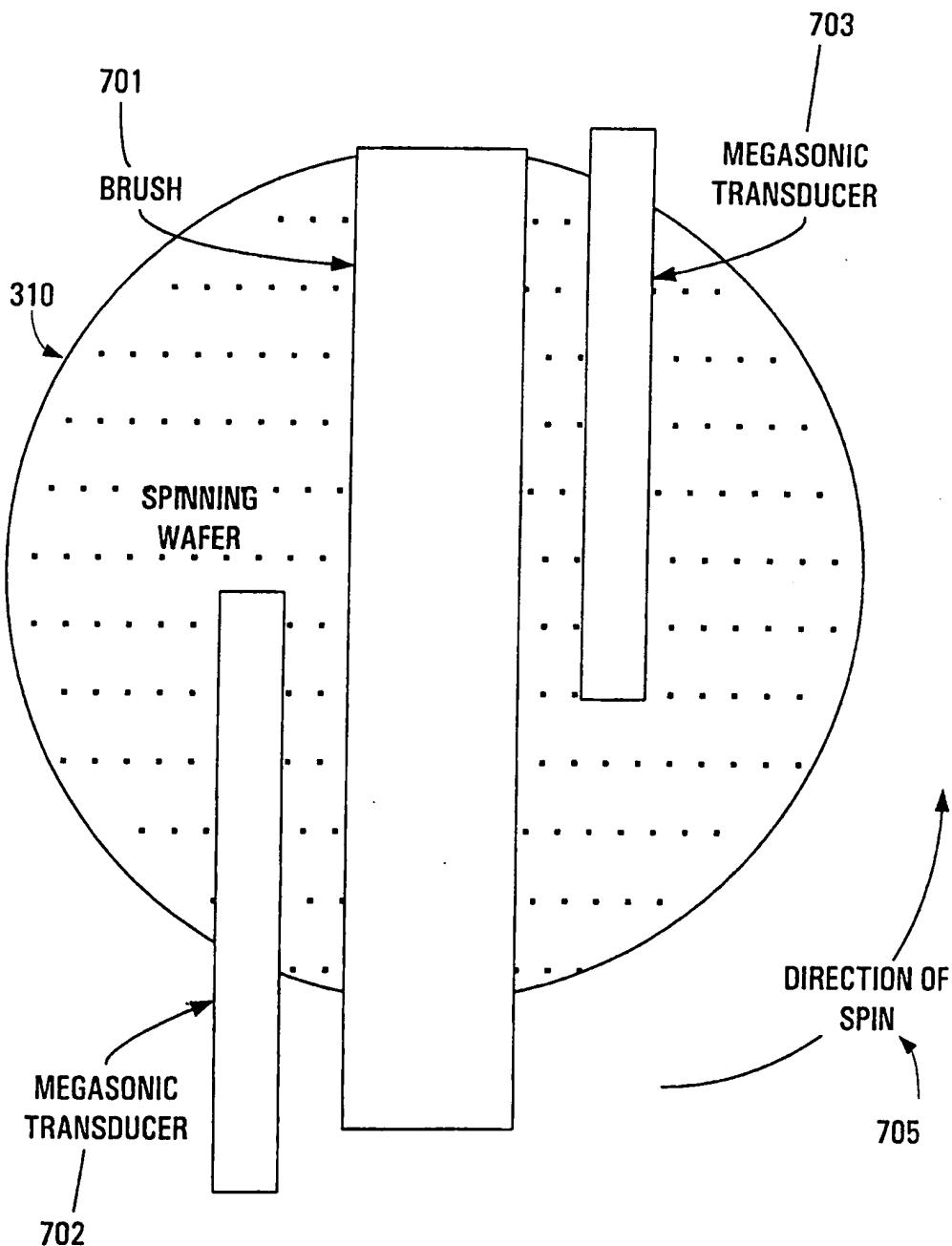
700

FIGURE 7B

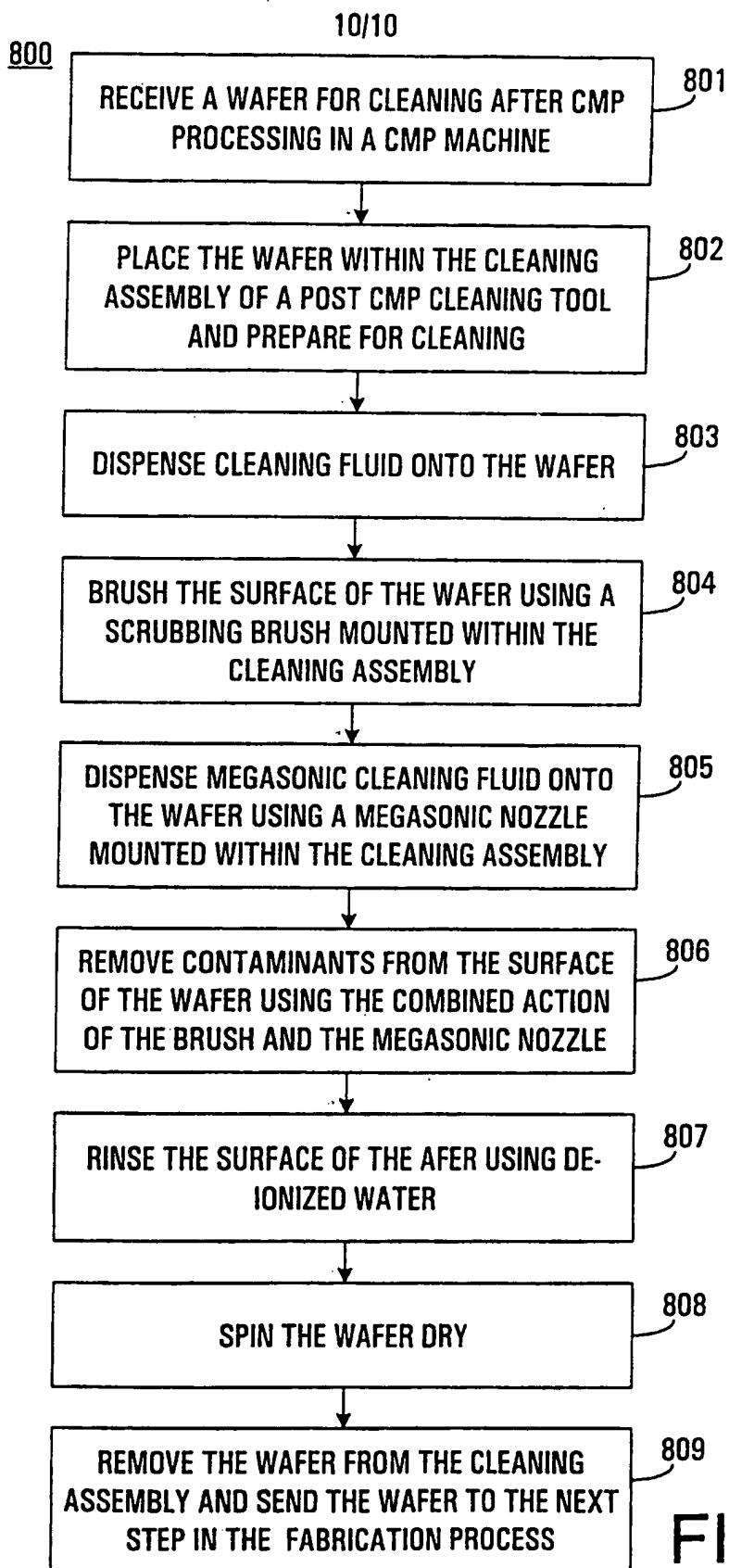


FIGURE 8

# INTERNATIONAL SEARCH REPORT

International Application No  
PCT/US 00/07394

**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 H01L21/00

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 1995, no. 06, 31 July 1995 (1995-07-31) -& JP 07 086218 A (DAINIPPON SCREEN MFG CO LTD), 31 March 1995 (1995-03-31) abstract ----	1-3, 6, 10-12
Y		5, 8, 15
X	PATENT ABSTRACTS OF JAPAN vol. 008, no. 103 (E-244), 15 May 1984 (1984-05-15) -& JP 59 019329 A (HITACHI SEISAKUSHO KK), 31 January 1984 (1984-01-31) abstract ----	1-4, 10, 11, 13
		-/-

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

\* Special categories of cited documents :

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\*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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\*&\* document member of the same patent family

Date of the actual completion of the international search

30 June 2000

Date of mailing of the international search report

12/07/2000

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**INTERNATIONAL SEARCH REPORT**

Int'l Application No
PCT/US 00/07394

**C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 1995, no. 06, 31 July 1995 (1995-07-31) -& JP 07 086222 A (TOKYO OHKA KOGYO CO LTD), 31 March 1995 (1995-03-31) abstract ---	1-3, 10, 11
X	PATENT ABSTRACTS OF JAPAN vol. 1998, no. 12, 31 October 1998 (1998-10-31) -& JP 10 189528 A (DAINIPPON SCREEN MFG CO LTD), 21 July 1998 (1998-07-21) abstract ---	1-4, 7, 10, 11, 13, 14
Y	US 5 868 866 A (MAEKAWA ET AL.) 9 February 1999 (1999-02-09) the whole document ---	5, 8, 15

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 00/07394

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JP 07086218	A	31-03-1995	NONE	
JP 59019329	A	31-01-1984	NONE	
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JP 10189528	A	21-07-1998	NONE	
US 5868866	A	09-02-1999	JP 8238463 A	17-09-1996